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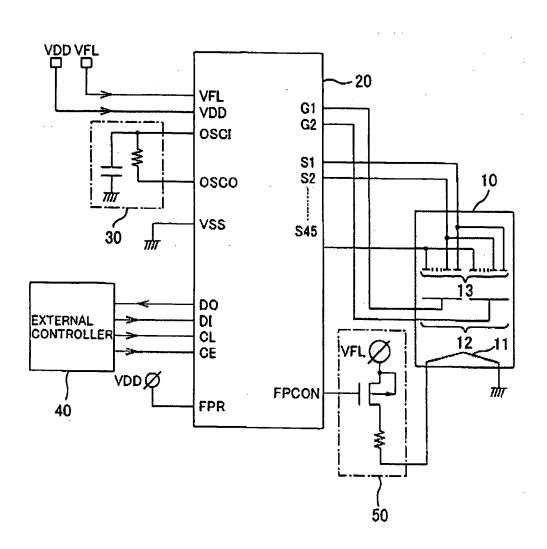
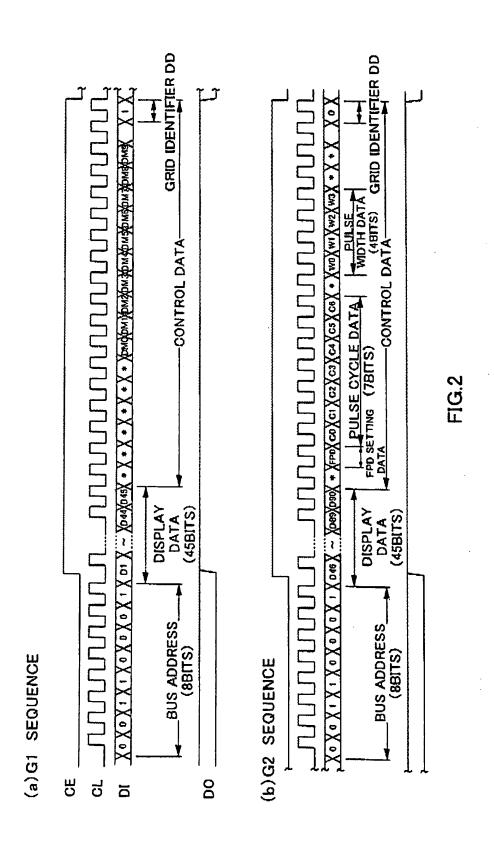
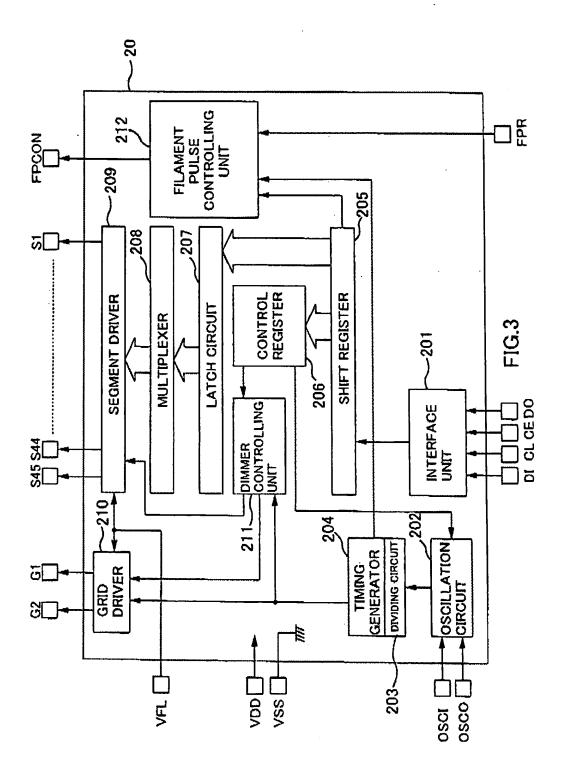


FIG.1

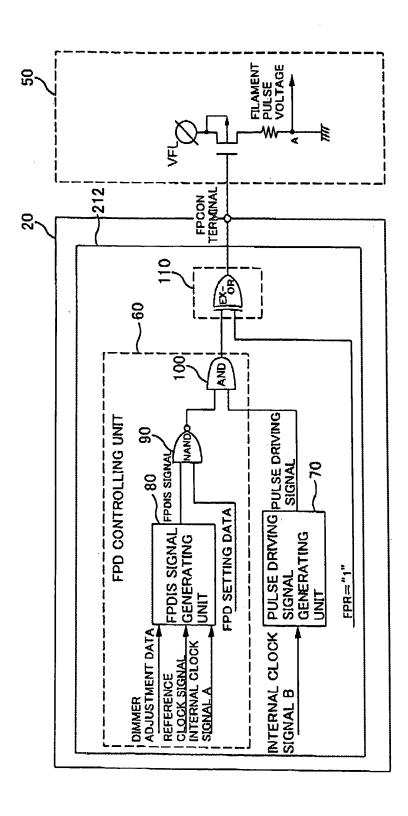
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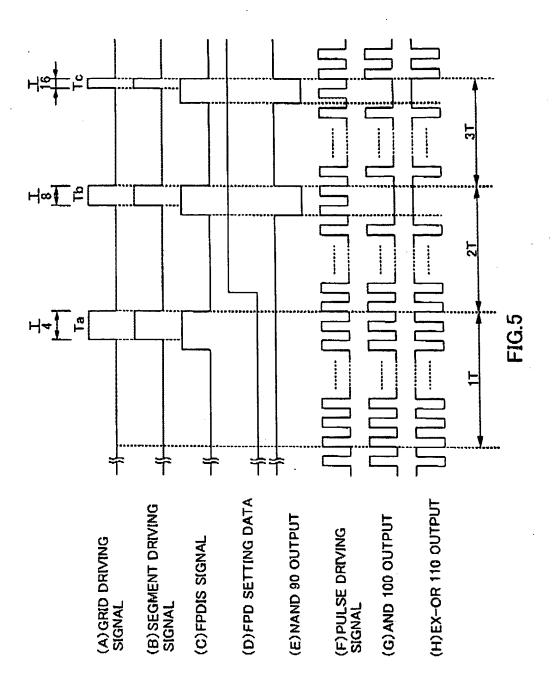


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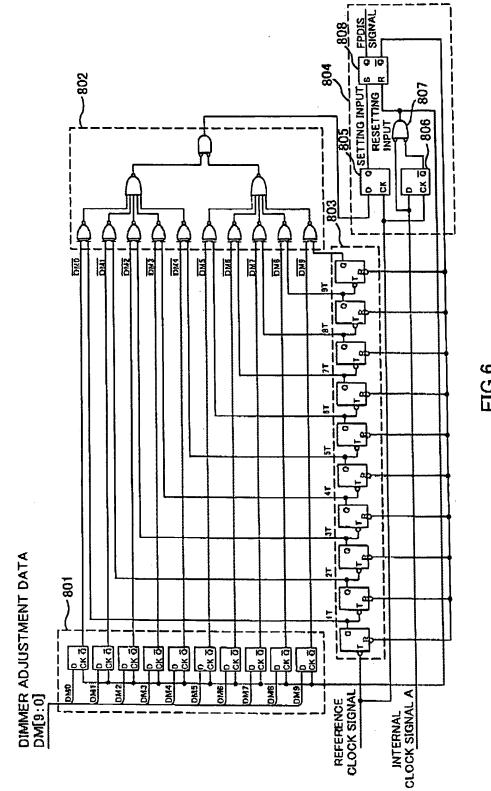


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(A) REFERENCE CLOCK SIGNAL

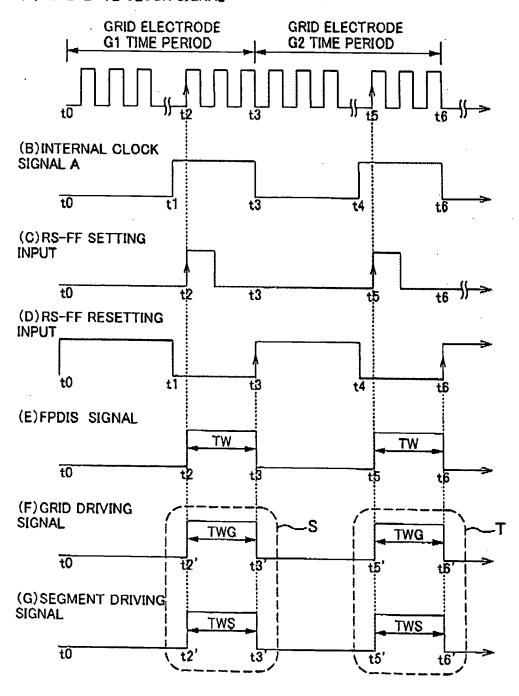


FIG.7

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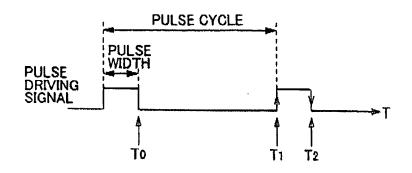


FIG.8

W3	W2	W1	wo	VALUE SET FOR Wn	FPC PULSE WIDTH
0	0	0	0		
0	0	0	1	3	3/fosc
0	0	1	0	4	4/fosc
					~
0	1	1	0	8	8/fosc
	~				
1	1	1	0	16	16/fosc
1	1	1	1	17	17/fosc

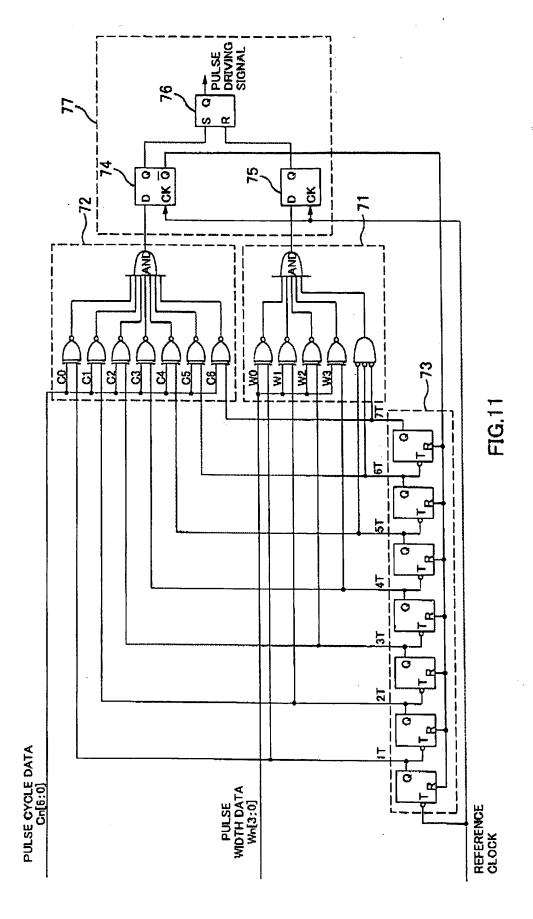
FIG.9

C6	C5	C4	C3	C2	C1	CO	VALUE SET FOR Cn	FPC PULSE CYCLE		
0	0	0	0	0	0	0				
0	0	0	0	0	0	1	_	_		
0	0	0	0	0	1	0	4	4/fosc		
0	0	0	0	0	1	1	5	5/fosc		
	~									
1	0	0	0	1	1	0	72	72/fosc		
	~									
1	1	1	1	1	1	0	128	128/fosc		
1	1	1	1	1	1	1	129	129/fosc		

FIG.10

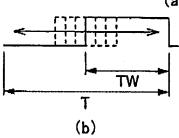
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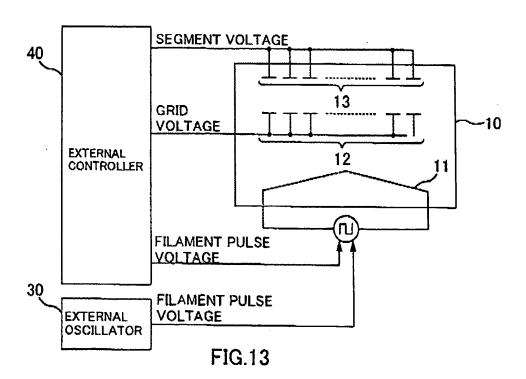
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0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	DM9	DM8	DM7	DM6	DM5	DM4	DM3	DM2	DM1	DM0	DIMMER VALUE (TW/T)
0 0 0 0 0 0 0 0 0 0 1 0 2/1024 1 1 1 1 1 1 1 1 1 1 0 0 11020/1024 1 1 1 1 1 1 1 1 1 1 1 0 1 1021/1024 1 1 1 1 1 1 1 1 1 1 1 0 1 1022/1024	0	0	0	0	0	0	0	0	0	0	0/1024
} 1	0	0	0	0	0	0	0	0	0	1	1/1024
1 1 1 1 1 1 1 1 0 1 1021/1024 1 1 1 1 1 1 1 1 0 1022/1024	0	0	0	0	0	0	0	0	1	0	2/1024
1 1 1 1 1 1 1 1 0 1 1021/1024 1 1 1 1 1 1 1 1 0 1022/1024						₹					1
1 1 1 1 1 1 1 1 0 1022/1024	1	1	1	1	1	1	1	1	0	0	1020/1024
1022/1024	1	1	1	1	1	1	1	1	0	3	1021/1024
1 1 1 1 1 1 1 1 1 IMPOSSIBLE TO SE	1	1	1	1	1	1	1	1	1	0	1022/1024
	1	1	1	1	1	1	1	1	1	1	IMPOSSIBLE TO SET



1;

FIG.12



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